

Amendment to the Specification

Please replace the paragraph beginning on page 9 line 27 and ending on page 10 line 4 with the following amended paragraph:

When the challenge response system 300 is triggered, a challenge activation component 340 [[350]] can send a challenge message to the sender 350 [[360]] of the questionable message. The challenge message can include a URL, for example, which when clicked by the sender 350, directs the sender 350 to a webpage. The webpage can include a puzzle or question that is easily and readily solvable by humans. The sender 350 submits his response to the puzzle or question to a response receiving component 360 [[370]] also located in the challenge response system 300. The sender's response can then be validated for its accuracy. If the response is correct, the message can be released, unblocked, or "un-hidden" in the user's inbox 310.

Please replace the paragraph on page 14 lines 1-12 with the following amended paragraph:

The system memory 816 includes volatile memory 820 and nonvolatile memory 822. The basic input/output system (BIOS), containing the basic routines to transfer information between elements within the computer 812, such as during start-up, is stored in nonvolatile memory 822. By way of illustration, and not limitation, nonvolatile memory 822 can include read only memory (ROM), programmable ROM (PROM), electrically programmable ROM (EPROM), electrically erasable programmable ROM (EEPROM), or flash memory. Volatile memory 820 includes random access memory (RAM), which acts as external cache memory. By way of illustration and not limitation, RAM is available in

many forms such as synchronous RAM (SRAM), dynamic RAM (DRAM), synchronous DRAM (SDRAM), double data rate SDRAM (DDR SDRAM), enhanced SDRAM (ESDRAM), Synchlink DRAM (SLDRAM), and direct Rambus RAM (DRRAM).